

EAST - [current.wsp:1]

FileViewEditToolsWindowHelp

Drafts

BRS:

ISNR:

BRS:

BRS: 708/490.ccls. and 6

BRS:

BRS:

Pending

Active

L1: (10537) array\$1 near3 (mac\$1 or processor\$1)

L2: (12022) local adj memory

L3: (16427) (share\$1 or global) adj memory

L4: (32) 1 same 2 same 3

L5: (260) 1 and 2 and 3

L6: (228) 5 not 4

L7: (121) 6 and (mac\$1 or multiply\$3)

L8: (107) 6 not 7

Failed

Saved

Favorites

Tagged (13)

UDC

Queue

Trash

Search

USPAT:US-PGPUB:EPQ:JPO.DERWENT:IBM:MDB:

Default operator

OR

6 not 7

	U:	Document ID:	Issue Date	Pages	Title	Current OR:	Current XRef	Retrieval Cl	
1	<input type="checkbox"/>	US 20040049672	20040311	31	System and method for hardware-software multitasking on a reconfigurable computing platform	713/100			Nollet
2	<input type="checkbox"/>	US 20040015735	20040122	44	Fault tolerant cell array architecture	714/10	714/25; 714/29;		Norma
3	<input type="checkbox"/>	US 20040015633	20040122	17	Signal processing resource for selective series processing of data in transit on communications paths in multi-processor arrangements	710/305			Smith
4	<input type="checkbox"/>	US 20040012563	20040122	24	Systems and methods of multi-pass data processing	345/157			Papak et al.
5	<input type="checkbox"/>	US 20030210686	20031113	70	ROUTER AND METHODS USING NETWORK ADDRESSES FOR VIRTUALIZATION	370/389	370/400		Terre
	<input type="checkbox"/>	US 20021022	20021022	22	Providing a register file memory with local addressing in a SIMD parallel	714/14	2002/220 06		Vin

Ready

Start

Inbox - Micro...

EAST - [cur...

EN 9:55 AM